ImageWriter

Microchip PIC12F Driver Parameter Module

ImageWriter Supports a configurable Microchip driver for devices in the PIC12F family. In certain circumstances you may wish to modify the parameters, especially the clock-frequency, to enhance system throughput or improve compatibility. Driver parameters are always reset and initialized to defaults when you select the Manufacuter:Device via a CIL command or in Terminal Mode.

If your device selection contains a p_MCT12F device object, you may load(examine) or program these parameters to non-default values. The mapping of the p_MCT12F object is detailed below.

You should not modify p_MCT12F driver parameters from their defaults without due reason and understanding of the target devices programming specifications. Modification of parameters may cause indeterministic algorithm or system behavior and prevent proper device programming.

Object for p_MCT12F

Object Name:	*Program / Execute*	*Verify*	*Load*	*Object Size (Bytes)*
p_MCT12F	●		●	0x08

Object Memory Map:

Byte 0	Byte 1	Byte 2	Byte3
CLK Delay (MSB)	**CLK Delay (LSB)**	**VPP_VDD Delay MSB**	**VPP_VDD Delay LSB**

Byte 4	Byte 5	Byte 6	Byte7
Reserved	**Reserved**	**Reserved**	**Reserved**

- CLK Delay is a 16-bit value expressed in uS. The physical clock frequency can be calculated as 1/(CLK Delay * 2).

- VPP_VDD Delay is a 16-bit value expressed in mS. Power-up default is 0. This value controls the delay between VPP rise and continuation of the algorithm. The purpose is to provide a delay to allow the customer to switch VCC following VPP, it situations where this mode is necessary. This conforms with Microchip's VPP-First Program Entry Mode.

- Reserved Bytes, these bytes are reserved for future use.